AMENDMENT AND RESPONSE UNDER 37 CFR § 1.116 - EXPEDITED PROCEDURE

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PARAMETER POPULATION OF CELLS OF A HIERARCHICAL SEMICONDUCTOR STRUCTURE VIA FILE RELATION Title:

## IN THE SPECIFICATION

The paragraph beginning at page 1, line 8 is amended as follows:

The present application is related to the co-filed, co-pending, and co-assigned application entitled "Hierarchical Semiconductor Structure Design," U.S. Serial No. 09/031,398, filed February 26, 1998, now U.S. Patent No. 6,449,757, which is hereby incorporated by reference.

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